This contribution describes heatsink technology as deployed by Würth Elektronik, based on the “maxon compact drive” application example. It also describes how heat dissipation can be improved through design measures.
Introduction

The increasing miniaturisation and power of electronic components is not only accompanied by mounting electromagnetic compatibility (EMC) requirements, a heat management concept also needs to be developed. Reliability studies have revealed that 50% of electronic system failures are caused by elevated temperature demands [1].

This means that critical temperatures should be avoided and components operated within their prescribed temperature range. The PCB becomes of particular importance for effective thermal management. Suitable heat dissipation measures should be considered as early as in the design and development phase, because subsequent modifications are generally more costly and involve an increased engineering effort.

This article describes the heat management option adopted by Würth Elektronik, based on the example of a motor control unit. Our technology is mainly deployed with circuit boards populated with power semiconductors or LEDs. These circuit boards are generally two-layer or multilayer, as the logic cannot be accommodated on single layer circuits.

Application example

A means of heat management through the circuit board itself, based on the example of the maxon compact drive control unit (Fig. 2) from the company maxon motor, is investigated as follows. A way was sought of integrating the control unit within the motor housing so as to arrive at a more compact construction. The control unit is usually placed outside the motor housing, as it is adversely affected by the motor temperature. This influence has to be minimised with a heat management solution, so that the motor functions reliably despite the control unit being integrated in the motor. In this case the control logic circuit board was produced as a heatsink circuit board. A heatsink circuit board is generally defined as a combination of circuit board and cooling element (Fig. 3).

Basics

There are three ways of transferring heat (Fig. 1). The first means of heat dissipation is convection. Convection is understood as the transfer of heat through gases and liquids (e.g. air and water). The heat is removed with the medium. The second type of heat transfer is the emission of infrared photons (radiation). The circuit board manufacturer has only limited means of influencing these two types of heat transfer. The third type of heat transfer is conduction through solids. The image shown in figure 1 illustrates a 4 layer PCB glued to an aluminium heatsink.

The heat management concept adopted by Würth Elektronik is a combination of vertical thermal conduction (through thermovias or a combination of microvias and buried vias) and horizontal thermal conduction (through the spread of heat in copper surfaces and/or laminated aluminium cooling element).

A thermovia is a hole located in the circuit board specifically for heat transfer. These holes should usually be located beneath the heat source to ensure direct dissipation of heat. Figure 4 shows the thermovias from the example. These thermovias are completely filled with resin and capped with copper so that they can be soldered upon without solder flowing away.
Air entrapment in the thermovias — which could have a negative effect on the soldering process — is avoided.

To illustrate the advantage of these thermovias, the thermal resistance of the solder surface shown in figure 4 is calculated, both with and without thermovias. In a further comparison, the thermal resistance of the thermovia region of buried vias and of laser microvias is calculated (Fig. 6 and 7). The calculation of thermal resistance is analogous to the calculation of electrical resistances connected in series and parallel (Equation 1 and 2).

**Equation 1**

\[
\text{R}_{\text{th}} \text{(series connected)} = \sum_{i} R_i
\]

**Equation 2**

\[
\frac{1}{R_{\text{th}} \text{(parallel connected)}} = \sum_{i} \frac{1}{R_i}
\]

Please note that this is only an estimate of the thermal resistance. The spread of heat outside of the solder area is not taken into consideration. A more precise calculation can be achieved by simulation or by generating an FEM model, however. The total thermal resistance of the control unit results as the sum of the individual thermal resistances (Fig. 5).

**Without thermovias, the thermal resistance of the solder area is given by:**

**Equation 3**

\[
\text{R}_{\text{th}} \text{(LP)} = \frac{d}{\lambda \cdot A} = 118 \frac{\text{K}}{\text{W}}
\]

\(d \text{ (copper)} = 280 \text{ µm}, \quad \lambda \text{ (copper)} = 360 \text{ W/mK;}
\]

\(d \text{ (FR4)} = 370 \text{ µm}, \quad \lambda \text{ (FR4)} = 0.30 \text{ W/mK;}
\]

\(A \text{ (solder area)} = 10.50 \text{ mm}^2.

The thermal resistance of the solder area with 9 thermovias for the maxon compact drive is calculated as:

**R\text{th} \text{(9 Vias)} = 7.4 \frac{\text{K}}{\text{W}}**

End Ø (thermovia) = 0.30 mm, \quad Thickness (copper sleeve) = 25 µm,

In comparison, the thermal resistance of the region with microvias and buried vias (Fig. 6 and 7).

**R\text{th} \text{(40 Microvias \& 9 Buried Vias)} = 3.7 \frac{\text{K}}{\text{W}}**

End Ø (microvia) = 0.100 mm

copper sleeve: Wall thickness = 25 µm

End Ø (buried Via) = 0.25 mm
Copper sleeve: Wall thickness = 25 µm

**Figure 4: Section through the thermovia region**

**Figure 5: Total resistance of the heatsink circuit board**

**Figure 6: Microvia region**
Tab. 1: Thermal resistances

<table>
<thead>
<tr>
<th>Thermal conductivity</th>
<th>Thermal resistance Rth</th>
</tr>
</thead>
<tbody>
<tr>
<td>Without thermovias</td>
<td>118 K/W</td>
</tr>
<tr>
<td>9 thermovias</td>
<td>7.4 K/W</td>
</tr>
<tr>
<td>microvias and buried vias</td>
<td>3.7 K/W</td>
</tr>
</tbody>
</table>

Tab. 2: Via configurations – circuit board thickness 1.60mm

<table>
<thead>
<tr>
<th>Pitch Array 10 x 10 mm</th>
<th>Number of vias End Ø 0.35 mm</th>
<th>Rth in K/W</th>
<th>Proportion of copper</th>
<th>Thermal conductivity W/mK</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>8.25</td>
<td>0.51 %</td>
<td>1.94</td>
<td></td>
</tr>
<tr>
<td>1.50 mm</td>
<td>49</td>
<td>3.39</td>
<td>1.18 %</td>
<td>4.15</td>
</tr>
<tr>
<td>1.10 mm</td>
<td>81</td>
<td>2.04</td>
<td>2.07 %</td>
<td>7.86</td>
</tr>
<tr>
<td>1.00 mm</td>
<td>100</td>
<td>1.65</td>
<td>2.55 %</td>
<td>9.70</td>
</tr>
<tr>
<td>0.90 mm</td>
<td>121</td>
<td>1.36</td>
<td>3.09 %</td>
<td>11.74</td>
</tr>
<tr>
<td>0.80 mm</td>
<td>169</td>
<td>0.98</td>
<td>4.29 %</td>
<td>16.57</td>
</tr>
<tr>
<td>0.60 mm</td>
<td>289</td>
<td>0.57</td>
<td>7.38 %</td>
<td>28.03</td>
</tr>
<tr>
<td>0.52 mm</td>
<td>400</td>
<td>0.41</td>
<td>10.21 %</td>
<td>38.30</td>
</tr>
</tbody>
</table>

Connection of the circuit board to the cooling element

Connecting the circuit board with a cooling element is a common method of cooling. In our example, this bond is achieved with transfer adhesive (Fig. 4). Table 3 shows an overview of thermal conductivity values for different materials.

Tab. 3: Thermal conductivity values of commonly used materials

<table>
<thead>
<tr>
<th>Material</th>
<th>Thermal conductivity [W/mK]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Silver</td>
<td>429</td>
</tr>
<tr>
<td>Cooper</td>
<td>360</td>
</tr>
<tr>
<td>Aluminium</td>
<td>204</td>
</tr>
<tr>
<td>Iron</td>
<td>73</td>
</tr>
<tr>
<td>Transfer adhesive</td>
<td>0.20 - 0.90</td>
</tr>
<tr>
<td>FR4</td>
<td>0.30</td>
</tr>
<tr>
<td>Air</td>
<td>0.026</td>
</tr>
</tbody>
</table>

Aluminium has become established as a cooling element material in circuit board practice. A common method is pressing with prepreg FR4 or the use of transfer adhesive. The advantage of using an adhesive rather than a prepreg lies in the dynamics during soldering. Permanently elastic adhesive can compensate for the difference in the expansion coefficients of aluminium and the circuit board by up to 300 % of its thickness.

In the application example, the cooling element was bonded with the circuit board under vacuum.

The thermal resistance of the transfer adhesive for the solder surface in the maxon compact drive example is given by

\[
R_{th\text{, transfer adhesive}} = 20.1 \frac{K}{W}
\]

\((A = 10.50 \text{ mm}^2; \lambda = 0.9 \text{ W/mK}; d = 0.19 \text{ mm})\)

Ideally, the reverse side of the circuit board is produced with a continuous copper layer, i.e. the heat is distributed over this plane and then passes to the cooling element via the transfer adhesive.

The thermal resistance is reduced to

\[
R_{th\text{, transfer adhesive}} = 0.12 \frac{K}{W}
\]

\((A = 1740 \text{ mm}^2; \lambda = 0.9 \text{ W/mK}; d = 0.19 \text{ mm})\)

Comparing with Equation 3; an increase in the surface area reduces the thermal resistance.

This example shows that an increase in the copper cross-section considerably lowers the thermal resistance.

Table 1 summarises the thermal resistances calculated. The poorest thermal conductivity is found on the version without thermovias. The best thermal conductivity is achieved with the use of microvias and buried vias.

Table 2 presents an overview of the alternative via regions and their thermal resistances.

Figure 7: Section through the thermovias

To be in a position to lower the thermal resistance even further, as many vias as possible should be located near the heat source. The aim is also to minimise the circuit board thickness and maximise the surfaces involved in the transfer of heat.
The release of heat from the cooling element to the housing takes place via the screw connections. These connections are facilitated by tapping threads in the cooling element. This produces an ideal means of heat transfer from the cooling element to the housing. This tapped hole is marked red in figure 3. A detailed view of the tapped hole may be seen in figure 9.

The curves represent the temperature of the electromotor windings (TW), the stator (TS), the board (TB) and of the housing (TH). The temperature TWmax is the maximum permissible temperature of the windings. The temperature difference between the circuit board (115°C) and the housing (105°C) is approx. 10°C.

This very good result underlines the good thermal conductivity properties of heatsink technology as presented here.

**Discussion**

In the selection of soldering method (e.g. reflow, wave soldering) for the heatsink solution described, the limitation applies that the reverse side cannot be used for SMD population. Population of the non-aluminium side with wired components does not present a problem if the cooling element used and the transfer adhesive are omitted from around the through-contacts of the components. Perfect heat dissipation hampers the soldering process! As a result of the very good heat dissipation, temperature management in the soldering process has to be adapted accordingly. Normally this means a longer preheating time and possibly higher temperatures, which means increased stress for the component and the circuit board. The precise soldering parameters have to be verified with solder tests for each application.

As described, the calculations performed here are not precise, but they are still sufficient as an estimate. This theoretical estimate with the calculations is a good way of determining the dimensioning of the circuit board layout. Graph 1 confirms this assumption.

In graph 1 you can see the temperature measurement curve for the example application. The measurement is performed using thermistors (Negative Temperature Coefficient resistors).

**The test conditions for the measurement are:**

- Motor current: \( I = 2.6 \text{ A} \)
- Speed: \( n = 5000 \text{ rpm} \)
- Power loss of the motor: \( P = 4.7 \text{ W} \)
- Ambient temperature: \( T = 25^\circ\text{C} \)
Literature


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