

SEMINAR INVITATION

on 24.04.2025 in **GENTBRUGGE**

Partner:





INVITATION TO THE FREE SEMINAR ON 24.04.2025 IN GENT

Würth Elektronik cordially invite you to the free Seminar: On the edge, 24.04.2025 in GENTBRUGGE.

The seminar was designed by engineers and technicians and is aimed at people who are interested in practice-oriented content, to get support in the development of error-free electronics and devices.

Main topics:

- Digital Twins of medical devices: a case study
- Gigabit Ethernet Interface: pitfalls & applications
- Al processing to detect rare cases

Seminar location:

Dekimo Gent N.V. Brusselsesteenweg 708 9050 Gent / Brugge

Are you interested in participating? If so, you are cordially invited to this seminar. You can register via the website www.we-online.com/seminarregistration. Participation is free of charge and your diner is included.

Should you be unable to attend after registration, please cancel free of charge via Nathalie.vanherck@we-online.com. This will help us a lot if you can do this one week before the event, if not, we will encounter unnecessary costs.

We are convinced that we can offer you an interesting seminar and look forward to your participation.

(The organization reserves the right to change the agenda and/or the location)

Kind regards,

Würth Elektronik eiSos GmbH & Co. KG & Dekimo

AGENDA FOR THE FREE SEMINAR: ON THE EDGE, 24.04.2025 IN GENTBRUGGE

15:00: Doors open

15:15: words of welcome

15:30: Digital Twins of medical devices: a case study (Hendrik De Vloed,

Dekimo)

Combining a 3D user interface and a firmware emulation layer to create

a virtual drug infusion pump, useful for debugging, training, and

marketing.

16:00: Gigabit Ethernet Interface: pitfalls & applications

(Raf Vleugels, Würth Elektronik)

In the design of an Ethernet interface, questions often arise concerning the shield connection of the cable and the front[1]end design, especially regarding the ground connections. Research on the Internet reveals various suggestions for the shield connection, where a 1 nF Y-capacitor is often suggested. However, the effect of the interface on performance and the EMC behavior are not described. During this session we will take a detailed look into the EMC properties of the Ethernet interface

with various shield connections and configurations.

16:30: Al processing to detect rare cases

(Jerome Schang, Alif Semiconductor)

NVIDIA TAO enablement and the usage of Omniverse technology to build synthetic data to train models for rare cases that are difficult to

train for rare cases that are difficult to train for.

17:00: Diner