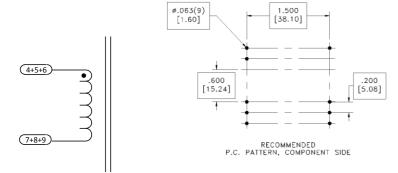


ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	4-7	tie(4+5+6,7+8+9), @20°C	0.135 ohms max.
INDUCTANCE	4-7	tie(4+5+6,7+8+9),50kHz, 100mV, Ls	280.0µH ±10%
SATURATION CURRENT	4-7	tie(4+5+6,7+8+9), 20% rolloff from initial	18A



GENERAL SPECIFICATIONS

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Customer to tie terminals 4&5&6 and 7&8&9 internally on PC Board.

Application of transformer allows for leadwires between terminals 4&5&6 and 7&8&9 to solder bridge.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

DFM		Packaging Specifications	}
DATE		Method: Tray	(())
ENG	YGD	PKG-TBD	Φ
REV.	00		CONVENTION PLACEMENT
DATE		www.we-online.com	n/midcom

	Tolerances unless	olerances unless otherwise specified:				
	Angles: ±1°	Decimals: ±.005 [.13]				
	Fractions: ±1/64	Footprint: ± .001 [.03]				
NΤ	This drawing is	dual dimensioned. Dimensions in				

brackets are in millimeters.

INDUCTOR

DRAWING TITLE

PART NO.

750342940

SPECIFICATION SHEET 1 OF 1