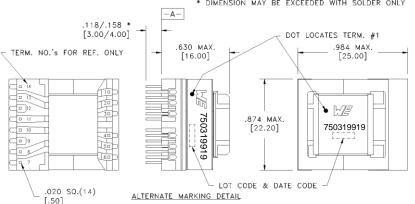


PART MUST INSERT FULLY TO SURFACE A IN RECOMMENDED GRID







ø.052(14) .787 [1.32] [20.00] .098(11) [2.50] [5.00] RECOMMENDED P.C. PATTERN, COMPONENT SIDE PRI SEC 32-48Vdc (11) 34V - 750mA 450kHz

Application of the transformer allows for the leadwires between terminals 3&4 and 10&11 to solder bridge.

Customer to tie terminals 3&4 and 10&11 on PC board.

ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	2-5	tie(3+4), @20°C	0.148 ohms ±10%
D.C. RESISTANCE	8-13	tie(10+11), @20°C	0.332 ohms ±10%
INDUCTANCE	2-5	tie(3+4),10kHz, 100mV, Ls	94.1µH ±10%
SATURATION CURRENT	2-5	tie(3+4), 20% rolloff from initial	3.25A
LEAKAGE INDUCTANCE	2-5	tie(3+4, 8+10+11+13), 100kHz, 100mV, Ls	3.0µH typ., 6.0µH max.
DIELECTRIC	2-13	tie(3+4, 10+11), 3000VAC, 1 second	3000VAC, 1 minute
TURNS RATIO	•	(13-8):(2-5), tie(3+4, 10+11)	1:1

GENERAL SPECIFICATIONS:

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Designed to comply with the following requirements as defined by IEC61558-2-16, and EN61558-2-16:

- Reinforced insulation for a primary circuit at a working voltage of 136Vrms, 1500Vpeak (operating frequency of <1MHz), Pollution Degree 2.



Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability. Marking method, font and color may vary on preproduction samples.

DFM SP Packaging Specifications Tolerances unless otherwise specified: DRAWING TITLE PART NO. DATE 11/3/2021 Method: Tray Angles: ±1° Decimals: ±.005 [.13] **TRANSFORMER** ENG PKG-0736 Fractions: ±1/64 Footprint: ± .001 [.03] JLV 750319919 REV. 00 This drawing is dual dimensioned. Dimensions in brackets are in millimeters. DATE 4/8/2022 www.we-online.com/midcom SPECIFICATION SHEET 1 OF 1