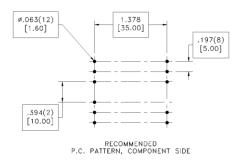


## ELECTRICAL SPECIFICATIONS @ 25° C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	2-5	tie(2+3, 4+5), @20°C	0.048 ohms ±20%
INDUCTANCE	2-5	tie(2+3, 4+5),10kHz, 100mV, Ls	270.00µH ±10%
SATURATION CURRENT	2-5	tie(2+3, 4+5)	19A
DIELECTRIC	2-Core	tie(2+3), 625VAC, 1 second	



## 2 3 4 4

## **GENERAL SPECIFICATIONS:**

OPERATING TEMPERATURE RANGE: -40°C to +125°C including temp rise.

Application of the transformer allows for the leadwires between terminals 2&3 and 4&5 to solder bridge.

Customer to tie terminals 2+3 and 4+5 on PC board.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability. Marking detail font and color may vary on preproduction samples.

DRAWING TITLE DFM Packaging Specifications PART NO. Tolerances unless otherwise specified: DATE Method: Tray Angles: ±1° Decimals: ±.005 [.13] **INDUCTOR** ENG PLP PKG-1241 Fractions: ±1/64 Footprint: ± .001 [.03] 750319521 REV. 01 This drawing is dual dimensioned. Dimensions in CONVENTION PLACEMENT brackets are in millimeters. DATE 2/8/2022 SPECIFICATION SHEET 1 OF 1 www.we-online.com/midcom