





QUICK START

Magl³C Power Module Evaluation Board for 1710X3801 LGA-12EP

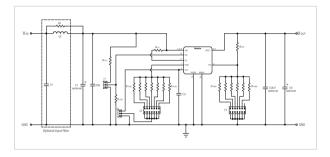
Evaluation Board 1780X3801

Version 1.0

SCHEMATIC

Features





The additional aluminum electrolytic capacitor C1 is only for evaluation board protection purposes. It is mounted at the termination of the supply line and provides slight damping of possible oscillations of the series resonance circuit represented by the inductance of the supply line and the input capacitance. It is not essential for operation.

For accurate V_{W} and V_{OUT} voltage measurements it is recommended to measure directly at the test pins placed beside the input and output capacitors C2 and C5. It is **not** recommended to use this evaluation board with input and output wire lengths longer than 1 m.

To optimize the EMI performance connect the R_{SW} resistor to VIN to enable spead spectrum behavior.

For the datasheet of the power module visit us at: https://www.we-online.com/ catalog/en/MAGIC-VDLM



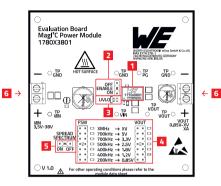
This product is highly sensitive to electrostatic discharge (ESD). As such, always use proper ESD precautions when handling, Failing to follow the aforementioned recommendations can result in severe damage to the part.



WARNING! - Before operating read the attached important notice document!

Ref. Des.	Description Order Code	Ref. Des.	Description Order Code		
U1	Magl ³ C Power Module (1710X3801)		Set by jumper	$30.1 \text{ k}\Omega$ for $V_{OUT} = 12 \text{ V}$ (1A/2A version)	
C1	Aluminum electrolytic capacitor 220 µF/50V (865060657012)	RFBB		64.9 kΩ for $V_{OUT} = 6 V$ (3A version)	
CIN	2 x Ceramic chip capacitor 4.7 μF/50V (885012209048)			$80.6 \mathrm{k\Omega}$ for $V_{\rm OUT} = 5 \mathrm{V}$	
сусс	Ceramic chip capacitor 1 µF/16 V (885012207051)			137 kΩ for V _{OUT} = 3.3 V 205 kΩ for V _{OUT} = 2.5 V	
соит	2 x Ceramic chip capacitor 47 µF/16 V (885012109011)			357 kΩ for V _{out} = 1.8 V (default setting)	
C9	Aluminum polymer capacitor			976 k Ω for V_{out} = 1.2 V	
	220 µF/16V (875115350002)			open for $V_{OUT} = 0.85 V$	
Cf	2 x Ceramic chip capacitor 4.7 μF/50 V (optional) (885012209048)			$18.2 \text{ k}\Omega$ for Fsw = 1.5 MHz (1A/2A version)	
Lf	Filter inductor, 1µH, PD2 (optional) (7447732010)			10 kΩ for Fsw = 1 MHz (3A version)	
R1	0Ω resistor bridge			$5.6 \mathrm{k\Omega}$ for Fsw = 700 kHz	
RENT	not mounted			$3.3 \mathrm{k\Omega}$ for Fsw = 500 kHz	
RENB	not mounted				
RPG	1 ΜΩ			0Ω for Fsw = 400 kHz	
RFBT	402 kΩ			(default setting)	
J1	"Jumper for EN connection to either VIN (device enabled) or GND (device disabled (61301421121)"			1.8 kΩ for Fsw = 200 kHz quency in continuous	
J2	Jumper for switching frequency selection. Only one resistor should be selected at a time (61301421121)	For Layout, Gerber and Step files visit us on www.we-online.com/ catalog/en/MAGIC-VDLM			
J3	Jumper for output voltage selection. Only one resistor should be selected at a time (61301421121)				
J4	Jumper to enable & disable spread spectrum behavior (61301421121)				

OVERVIEW



Absolute maximum ratings

Caution: Exceeding the abs. max. values given in the datasheet may affect the device negatively and may cause permanent damage.

This evaluation board is intended to be operated in a research and development environment under the supervision of qualified technicians and engineers who are trained and experienced in the safe use of electronics. This evaluation board was designed and tested according to CISPR32 Class B standards under Würth Elektronik laboratory test conditions, as indicated in the data sheet of

Description

- V_{IN} 3.5 38 V V_{0UT} -0.85 V to 6 V at I_{0UT} = 3A -0.85 V to 13 V at I_{0UT} ≤ 2.5 A I_{0UT} 1A/2A/3A
- 1 VDLM Variable Step Down LGA Module LGA12-EP
- 2 Jumpers (J1) for ENABLE & shut off the Module
- 3 Resistors to set UVLO level
- Jumpers to set predefined output voltage V_{out} and fsw
- 5 Jumpers to enable & disable spread spectrum behavior
- 6 Terminal block screw connectors for V_{IN} and V_{OUT}

the corresponding power module. Operation in other test setups may cause unintended electrical behavior and exceed the stated performance and limits imposed by the CISPR32 Class B standards. This evaluation board is not intended for usage in final applications. This evaluation board is not intended for resale.



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